Ref No: IITPKD/CIF/SS/077/2017   DT: 13.12.2017

Clarifications for IITPKD/CIF/SS/077/2017

Vector Network Analyzer

Point 31. [Material Measurement Upgradable Capability]
Should read as Material Measurement Capability and is mandatory.
Also, software should support algorithms **like** NRW **or** NIST **or** polynomial fit.

Point 18. [Maximum test port input level] and Point 32 [Source, Receiver Attenuator and Bias Tees]
We require at atleast two source ports and two receiver ports. Receiver step attenuators must be available on the receiver ports. Source step attenuators should be there on the source ports.

Signal Analyzer
Point 16. [Amplitude accuracy]
Amplitude accuracy is understood to be for the analysis bandwidth.

Microwave Analog Signal Generator
Phase noise in point 8 and 9 are specified at +8 dBm and harmonics in point 10 and 11 are specified at +8 dBm.
Harmonics < –30 dBc @ 40 GHz (+15 to +35 °C, at +8 dBm).

Due Date of the tender: 22.12.2017 at 2.00 PM

Technical bid Opening of the tender: 22.12.2017 at 2.30 PM

REGISTRAR, IIT PALAKKAD